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SUMMARY

With more than 17 years of Electrical and Electronic, Printed Circuit Board (PCB) layout design experience, I am an experienced Advisory Engineer & Head of B2B Projects (Technical) and an effective team leader. With good team-building skills, I am experienced in troubleshooting complex design problems to complete jobs on time. I have a reputation for completing design projects ahead of schedule. I am a resolution person, skilled in applying logical and creative approaches to solve problems.

PERSONAL INFORMATION

DOB	05-04-1986
CNIC NO	35202-0197855-3
GENDER	MALE
RELIGION	ISLAM
NATIONALITY	PAKISTANI

HAND ON WORK

- **SCHEMATIC TOOLS:** CADENCE DESIGN TOOL ORCAD SCHEMATIC FOR SCHEMATIC WORK. ALTIUM DESIGN TOOL ALTIUM DESIGNER FOR SCHEMATIC WORK.
- **LIBRARY DESIGNING TOOLS:** CADENCE DESIGN TOOL ALLEGRO 15. X AND 16. X FOR LIBRARY DEVELOPMENT MENTOR GRAPHIC LP WIZARD FOR LIBRARY
- **LAYOUT DESIGNING TOOLS:** CADENCE DESIGN TOOL ALLEGRO 14. X, 15. X AND 16. X FOR PLACEMENT, ROUTING, AND GERBER OUT. ALTIUM DESIGN TOOL ALTIUM DESIGNER FOR PLACEMENT, ROUTING, AND GERBER OUT.
- **GERBER TOOLS:** PENTALOGY TOOL VIEW MATE FOR GERBER VIEWING. DOWNSTREAM TOOL CAM350 FOR GERBER VIEWING. CAM350 FOR GERBER VIEWING AND OPEN/SHORT TEST

WORK EXPERIENCE

● Lead Electrical Design

Byonyks Lahore, Pakistan

Nov 2021 to till date

- Responsible for designing project-oriented electrical systems. (Also working under USA-based electronics consultants' mentorship to develop a custom design carrier board.)
- Responsible to lead the designing of the schematic for all PCB layouts.
- Designing drivers, multilayer PCB interfacing boards, sensors, actuators, and similar mechanisms.
- Detailed design, working with and from documentation and drawing.
- Developing scope documentation, circuit drawing, and test beds.
- Responsible to design all complex electrical systems when required.
- Responsible for the whole team's coordination & effective communication to identify and address problems and bottlenecks so that no delays occur in any phase.
- Responsible for ensuring that all team members use proper PPE during work wherever required.
- Responsible to ensure that all records are maintained in Byonyks systems.
- Conduct technical selection interviews of staff needed to be hired in his department.
- Involved in R&D for designing of sensors and supporting mechanical and Software teams.
- Leading the role of compliance with international standards (IEC 60601-1, IEC 60601-2-39, IEC 60601-1-2, IEC 60601-1-11).
- Working closely with the Mechanical team for design decisions.

● Advisory Engineer & Technical Lead

Blue East Lahore, Pakistan

Jun 2016 Nov 2021

- Training of new inductions and associate engineers on the assembly line.
- Lead a team of PCB designers.
- Supervise the production process for the initial batch of new PCBs on a production line.
- Visit project sites to propose implementation methods of our solutions.
- Sourcing of appropriate parts for the PCB.
- Suggest and refine installation procedures.
- Coordination with schematic and firmware teams for the implementation.
- Playing a key role in closing B2B Sales Projects.
- Prepare the team for development and survey. Successfully deploying more than 900 smart AC kit projects in renowned companies in Pakistan.
- Liaison between Blue East Pvt Ltd. and other corporate companies for sales, survey, implementation, and services.
- Successfully completed 15 commercial products.
- Provide technical support to corporate clients.
- On-site technical support to junior engineers or technical staff

- **STACK-UP CALCULATION:** POLAR INSTRUMENTS TOOL SB200 AND SI8000 FOR STACK-UP CALCULATIONS AND IMPEDANCE CONTROL CALCULATIONS. STACK-UP ADVISOR A FEATURE OF THE CADENCE DESIGN TOOL ALLEGRO 15. X AND 16. X FOR IMPEDANCE CALCULATIONS.
- **LAYOUT DESIGNING:** CADENCE DESIGN TOOL ALLEGRO 1415.1, 15.2, 15.5.1, 15.7, 16.0.1, 16.2, AND 16.3 FOR PLACEMENT, ROUTING, AND GERBER OUT. MENTOR GRAPHICS TOOL PADS FOR PLACEMENT, ROUTING, AND GERBER OUT.
- **LIBRARY DESIGNING:** CADENCE DESIGN TOOL ALLEGRO 15.1, 15.2, 15.5.1, 15.7, 16.0.1, 16.2, AND 16.3 FOR LIBRARY DESIGNING AND DEVELOPMENT.
- **SCHEMATIC TOOLS:** CADENCE DESIGN TOOL ORCAD SCHEMATIC CAPTURE 7.2 9.2.2 CIS AND CONCEPT HDL FOR SCHEMATIC WORK. MENTOR DESIGN TOOL VIEW LOGIC FOR SCHEMATIC WORK.
- **GERBER TOOL:** PENTALOGIX TOOL VIEW MATE 10.2 FOR GERBER VIEWING. GENESIS VER 8.2 FOR GERBER VIEWING AND OPEN/SHORT TEST.
- **PLATFORM:** WIN98, WINXP, 2000 PROFESSIONAL. MICROSOFT OFFICE.

PROJECTS

CONSUMER/SMART HOME PROJECTS

- ♦ AIR 1
- ♦ AIR 3
- ♦ AIR 4
- ♦ AIR 5
- ♦ AIR CLEAN
- ♦ PEST DEFENSE

SMART HOME SOLUTIONS

- ♦ ONE GANG SMART SWITCH
- ♦ TWO GANG SMART SWITCH
- ♦ THREE GANG SMART SWITCH
- ♦ SMART SOCKET

SMART 3-PHASE ENERGY METERS

SMART WATER DISPENSER DEVICE

- ♦ SMART WATER DISPENSER KIT

SMART HVAC CONTROL DEVICE

- ♦ UNIVERSAL SMART AC CONTROLLER +
- ♦ ENERGY MONITORING DEVICE

ENVIRONMENT & AQI MONITORING DEVICE

- ♦ TEMPERATURE, HUMIDITY & AIR QUALITY
- ♦ MONITORING DEVICE

ACCESS/ATTENDANCE SOLUTION

- ♦ SMART FACIAL ATTENDANCE /ACCESS DEVICES
- ♦ SMART BIOMETRIC ATTENDANCE /ACCESS DEVICES
- ♦ SMART NETWORK DOOR CONTROLLER\

● Senior Layout Designer

Powersoft 19, Lahore, Pakistan

Oct 2013 to Jun 2016

- Convert paper design to neat CAD-based schematics.
- Designs from single to multi-layer PCBs as per IPC design standards and manufacturability requirements.
- Surface mount, BGA, and through-hole designs with blind and buried vias.
- Split ground planes and power planes for mixed analog and digital designs.
- Provide High-Speed design consulting for placement using early analysis and design optimization at the system level.
- Detailed post layout for Impedance matching.
- Troubleshoot the multilayer PCB design boards.
- Challenges involve highly complex electrical, mechanical constraint-driven designs (requiring 100% manual routing, impedance-controlled stack-ups, Gbs interfaces).



Assistant PCB Design Engineer I

Global Engineering Services

Apr 2005 to Sep 2013

- Responsibilities include designing PCB projects from design board to end-product
- Gerber data output, taking into account the state-of-the-art designing tools for rigid boards using Cadence Allegro
- Jobs handled,
- Convert paper design to neat CAD-based schematics.
- Designs from single to multi-layer PCBs as per IPC design standards and manufacturability requirements.
- Surface mount, BGA, and through-hole designs with blind/buried and via-in-pad technology. Split ground planes and power planes for mixed analog and digital designs.
- Provide High-Speed design consultancy for placement, using early analysis and design optimization at the system level.
- Challenges involve
- Highly complex electrical, mechanical constraint-driven designs (requiring 100% manual routing.
- Impedance controlled stack-ups, up to 10Gbs interfaces).
- High-density interconnects (> 32 layers, Blind/Buried/Micro Vias and PTH via in-pad technology), packaging (200+ pins/sq inch, PTH, SMT, Mixed), meeting client's SI, EMC, DFM, DFA, DFT and Time to Market requirements just like pressure cooker situations.
- Prepared fabrication and assembly notes.
- Generated the Gerber package for the fabricator. Once the package has been generated, verify for open/short by running this test in Genesis before sending it to the fabricator
- Worked on projects containing 50,000 connections and more than 10,000 components.
- Experience working with leading PCB Fabricators in the US market like Sanmina, DDI, Titan, and Flextronics gives me a clear idea of their DFM rules and potential issues. Clients include Cisco Networks, Xilinx, Foundry Networks, Phillips, NXP Semiconductors, HP, Pillar Data Systems, Apple Computers, IBM, HiFN, Intel, Zultys, Brilliant Telecom, W&W Communication, Hansen Medical, Cyan Optics, LAB126, Sun Microsystems, LSI, Nouva Systems, etc. Worked on a variety of projects like Communication, IT & computing, Analog, RF, high-speed multi-layer digital designs & Medical projects, etc

Library Development Coordinator

Layout Designing

Cadence Design Tool ALLEGRO 1415.1, 15.2, 15.5.1, 15.7, 16.0.1, 16.2, and 16.3 for Placement, Routing and Gerber out. Mentor Graphics Tool PADS for Placement, Routing, and Gerber out

ENERGY MANAGEMENT SOLUTION

- ♦ SMART 3 PHASE / 2 LOADS / 5 LOADS / 5PHASE METERING DEVICE

GENSET MONITORING SYSTEM SMART

GENERATOR & FUEL MONITORING

MANAGEMENT WEB APPLICATION

INDUSTRIAL PROJECTS

- ♦ INDUSTRIAL GATEWAY CONTROLLER

SMART BIKE TRACKING DEVICE

- ♦ SBTD (BIKE 70CC)

SKILLS

- ♦ PROFICIENT IN MICROSOFT OFFICE
- ♦ PROFESSIONAL DIPLOMA IN
- ♦ COMPUTER HARDWARE (PDCH)
- ♦ BASIC AND DIGITAL ELECTRONICS,
- ♦ DIPLOMA IN COMPUTER INFORMATION TECHNOLOGY (C-I-T).
- ♦ PROFICIENT IN WINDOWS 98/2000/XP
- ♦ ALLEGRO (DESIGNER PCB SOFT WEAR) 15.2 – 16 VER.
- ♦ PROFICIENT IN MICROSOFT OFFICE, INTERNET EXPLORER, OUTLOOK EXPRESS, CUTE FTP, WINZIP 8.0
- ♦ COMMUNICATION SKILLS
- ♦ ORGANIZATIONAL KNOW-HOW
- ♦ LEADERSHIP
- ♦ ANALYTICAL SKILLS
- ♦ ADAPTABILITY
- ♦ CUSTOMER SERVICE
- ♦ PROBLEM-SOLVING ABILITIES

LANGUAGES

- ♦ ENGLISH
- ♦ URDU
- ♦ PUNJABI

Library Designing

Cadence Design Tool ALLEGRO 15.1, 15.2, 15.5.1, 15.7, 16.0.1, 16.2 and 16.3 for Library designing and development.

Schematic Tools

Cadence Design Tool OrCad Schematic Capture 7.2 9.2.2 CIS and Concept HDL for schematic work. Mentor Design Tool View logic for schematic work.

Gerber Tool

Pentalogix Tool View Mate 10.2 for Gerber viewing. Genesis ver 8.2 for Gerber viewing and open/short test.

Platform

WIN98, WINXP, 2000 professional. Microsoft Office.

● Supervisor Engineer

Systems International (U.P.S Solutions & Service provider)

Oct 2004 to Jan 2005

- ♦ Installing UPS all over Pakistan. Clients include the Bank of Punjab, Muslim Commercial Bank, PHP Pharmacy, IT Department of Punjab University, Global Travels, etc.
- ♦ Troubleshooting defective U.P.S systems and associated parts on site.
- ♦ Experienced in repairing defective on / offline U.P.S equipment

● Service Engineer

Systeck (U.P.S Solutions & Service provider)

Jun 2004 to Sep 2004

- ♦ Installation of Uninterrupted Power Supplies (U.P.S) all over Pakistan.
- ♦ On-site troubleshooting of defective U.P.S systems and associated parts
- ♦ Hands-on experience repairing Defective on / offline U.P.S System
- ♦ The experimental investigation of the components and devices characteristics.

EDUCATION

● Bachelor of Technology in Electronics,(B-Tech)

Preston University, Lahore Pakistan

2007–2008

● DAE (Diploma Of Associate Engineer DAE Electronics)

Board of Technical Education, Lahore Pakistan

2011–2004

Matric Science

● Govt. Pak Model High School. Lahore Pakistan

1999–2001

REFERENCES

Will be furnished on demand